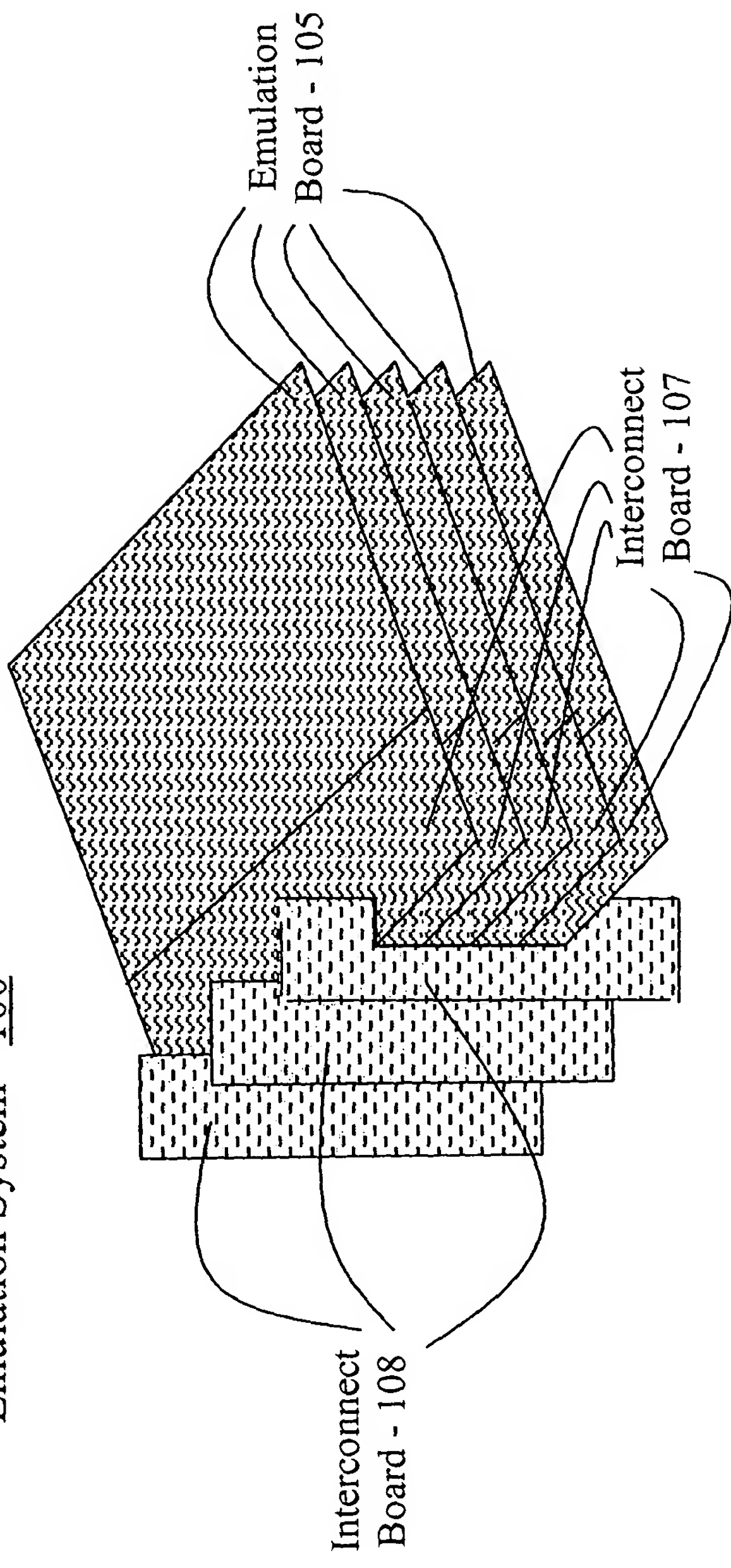


FIGURE 1

Emulation System - 100



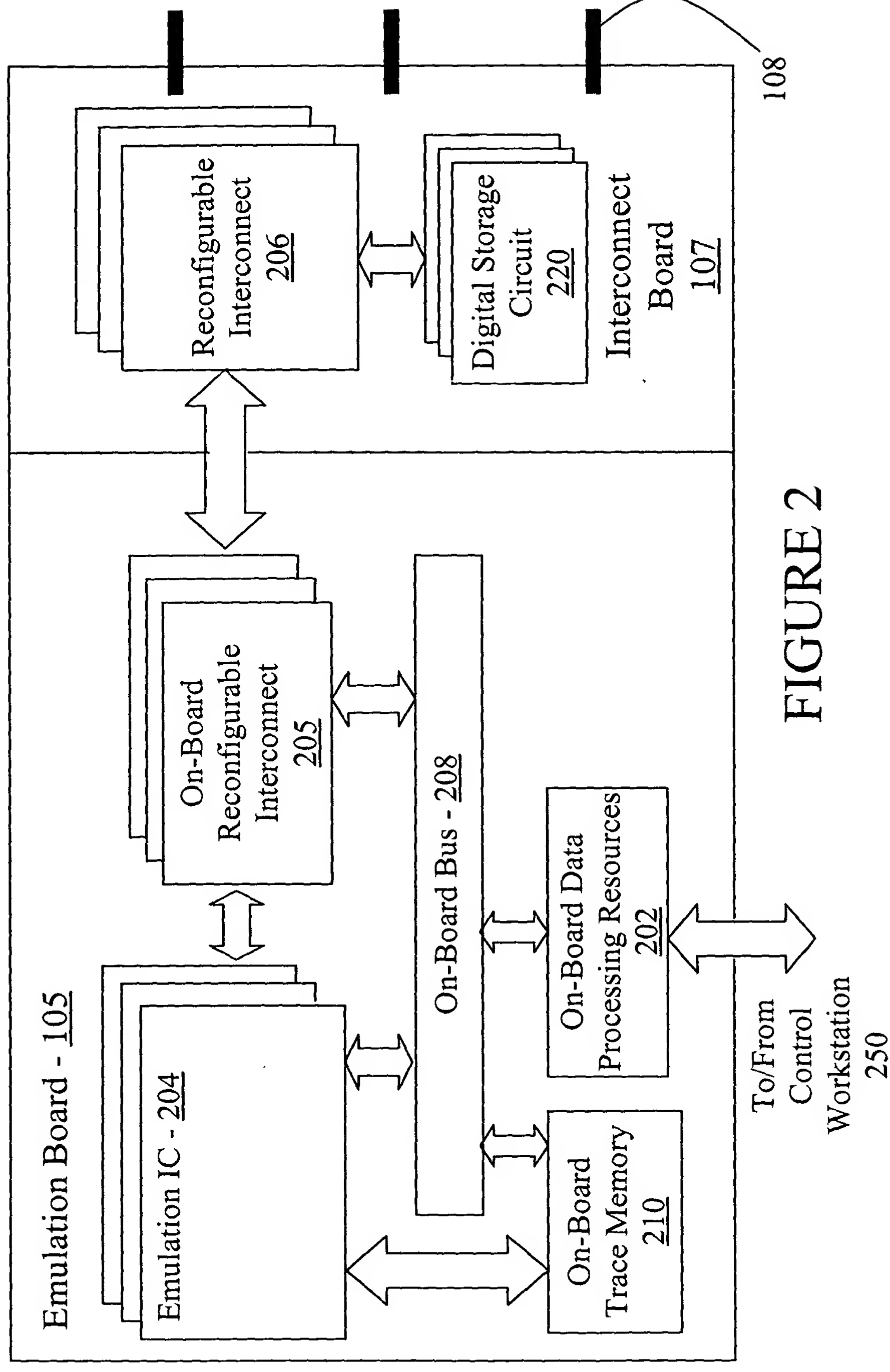


FIGURE 2

Figure 3

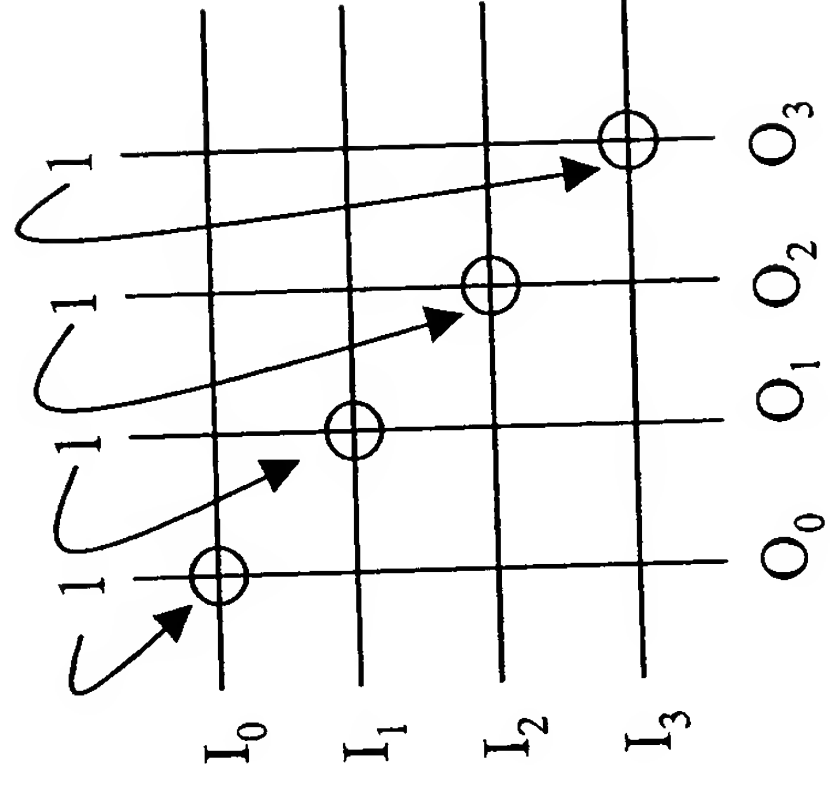
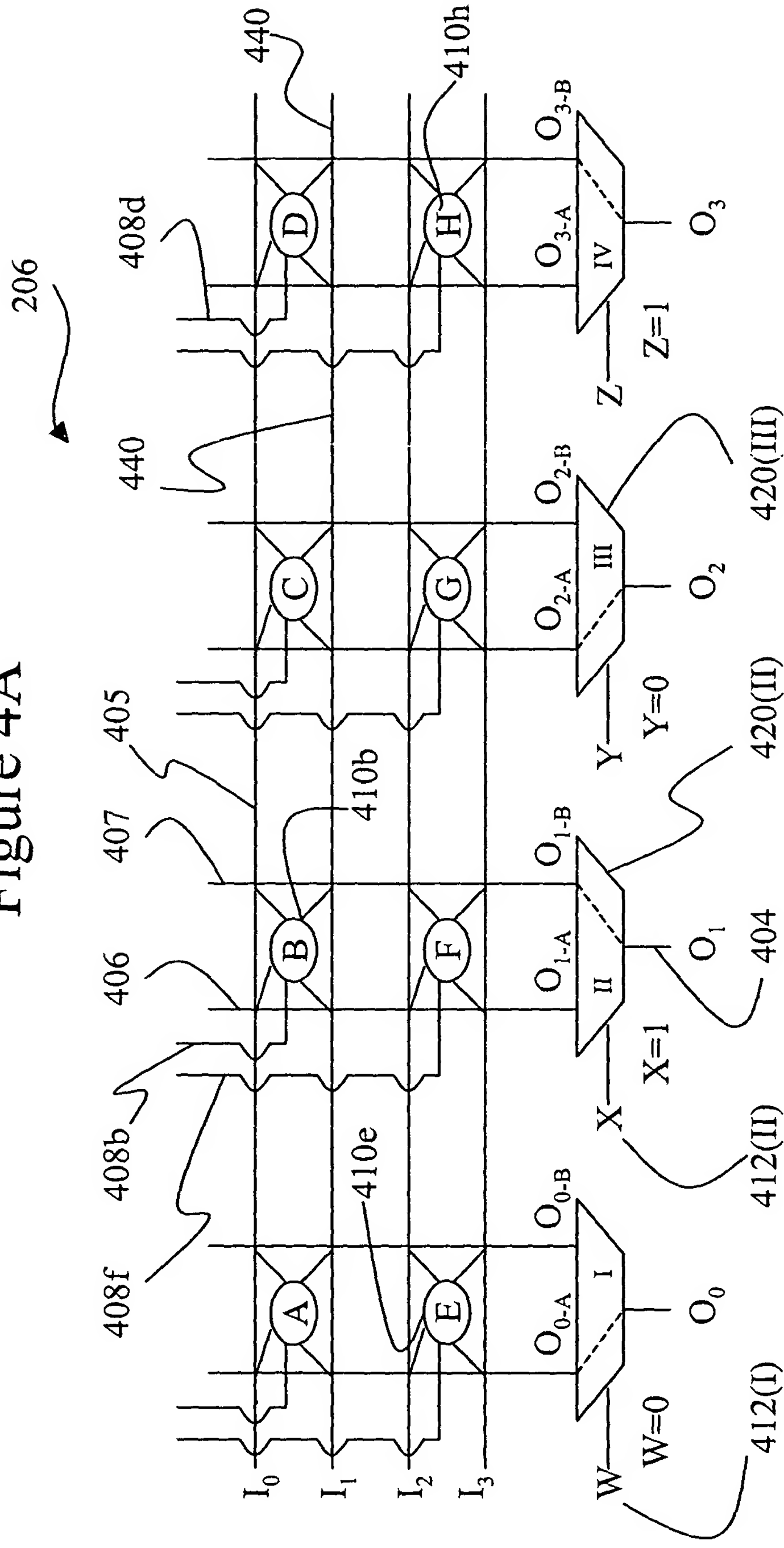


Figure 4A



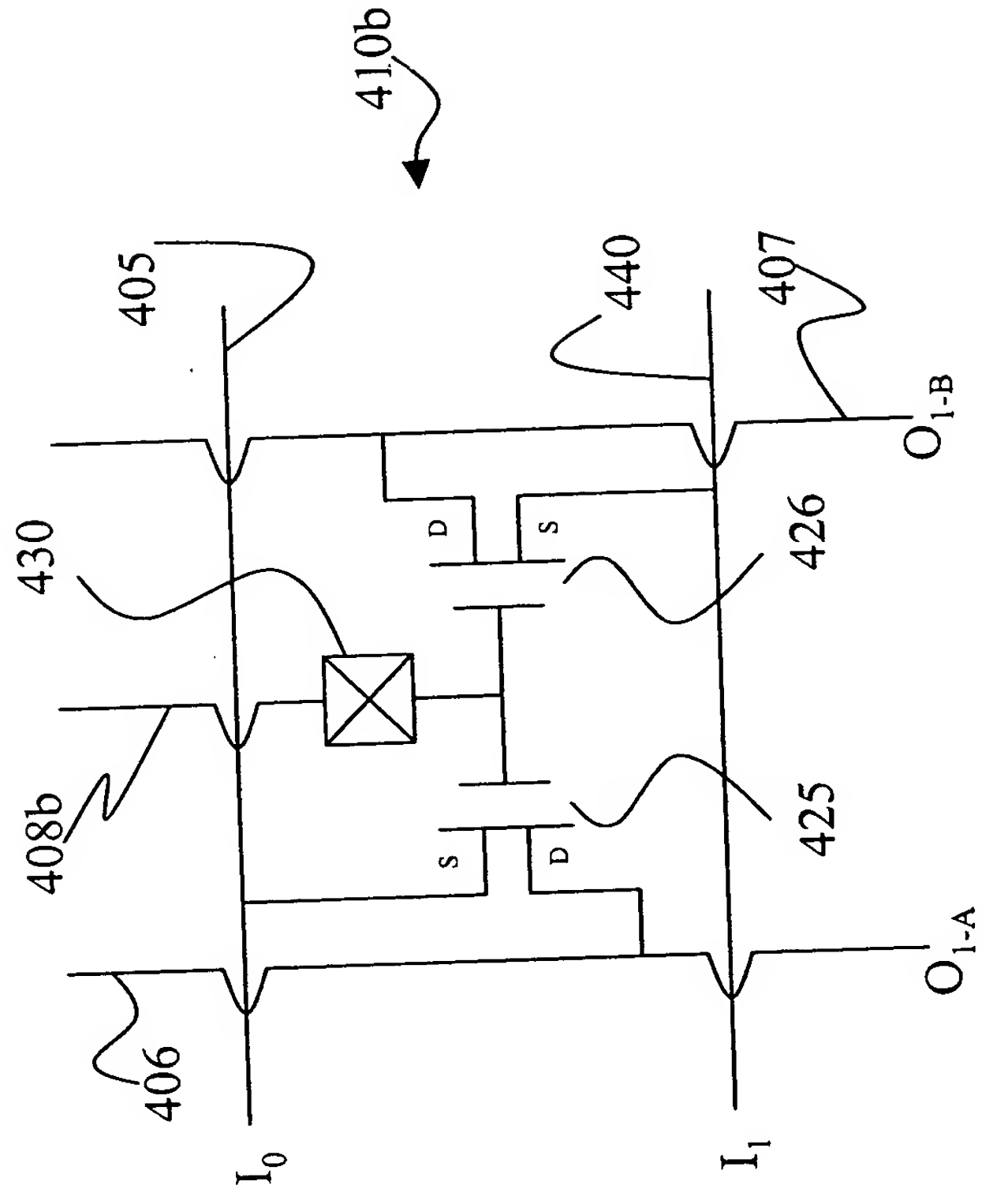


Figure 4B

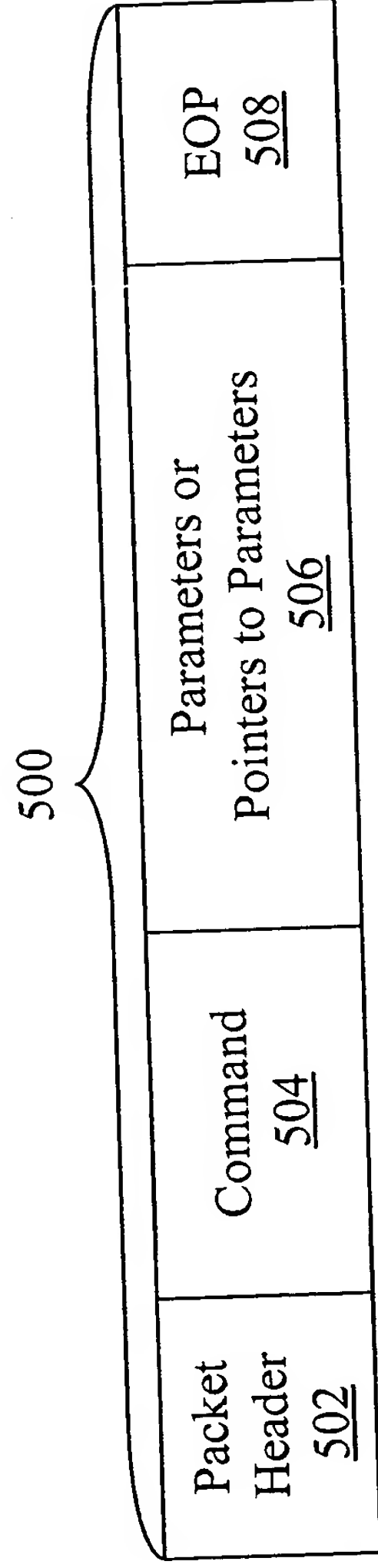


Figure 5

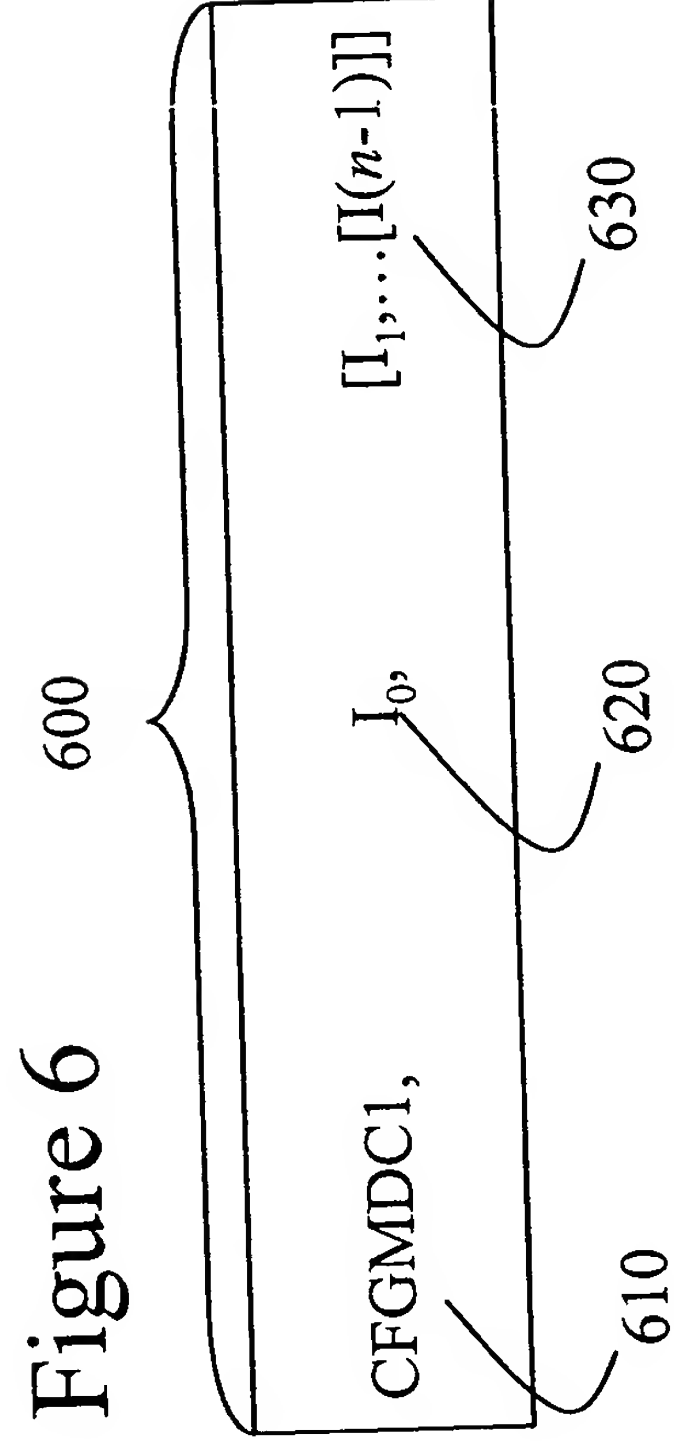


Figure 6

| Index Value | Number Of Bits | Config. Bit Table Row | Number of Output(s) (n) | Corresponding Output |
|-------------|----------------|-----------------------|-----------------------------|----------------------|
| 702 | | | | |
| 714 | 1 | 1 | 2 | O_0 |
| | 1 | 2 | 2 | O_1 |
| 718 | | | | |
| 720 | 2 | 1 | 3 or 4 | O_0 |
| | 2 | 2 | 3 or 4 | O_1 |
| 716 | 2 | 3 | 3 or 4 | O_2 |
| 722 | 2 | 4 | 4 | O_3 |
| | | | | |
| 000 | 3 | 1 | 5,6,7 or 8 | O_0 |
| 001 | 3 | 2 | 5,6,7 or 8 | O_1 |
| 010 | 3 | 3 | 5,6,7 or 8 | O_2 |
| 011 | 3 | 4 | 5,6,7 or 8 | O_3 |
| 100 | 3 | 5 | 5,6,7 or 8 | O_4 |
| 101 | 3 | 6 | 6,7 or 8 | O_5 |
| 110 | 3 | 7 | 7 or 8 | O_6 |
| 111 | 3 | 8 | 8 | O_7 |
| ... | ... | ... | ... | ... |

Figure 7


800 

Figure 8

Figure 8

| | 818 | 816 | 814 | 812 | |
|-----|-----|-----|-----|-----|-----|
| ... | 0 | 0 | 0 | 1 | ... |
| ... | 0 | 0 | 0 | 0 | ... |
| ... | 0 | 0 | 1 | 0 | ... |
| ... | 0 | 0 | 0 | 0 | ... |
| ... | 0 | 1 | 0 | 0 | ... |
| ... | 0 | 0 | 0 | 0 | ... |
| ... | 1 | 0 | 0 | 0 | ... |
| ... | ... | ... | ... | ... | ... |

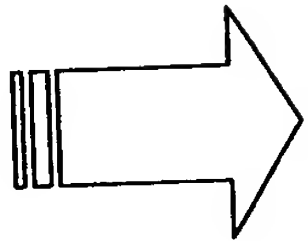
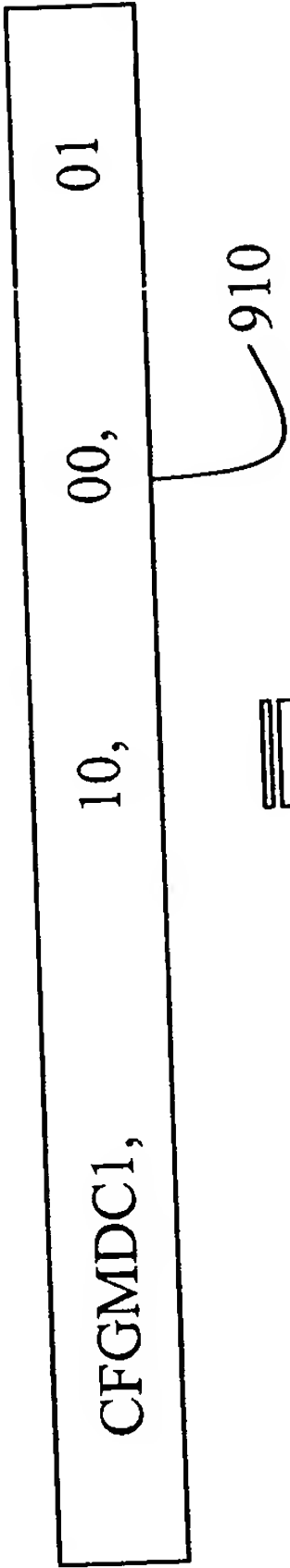


Figure 9

| | | | | | | |
|-------|-------|-------|-------|-------|--|-----|
| | | | | | | |
| I_1 | 0 | 0 | 0 | 1 | | 920 |
| I_2 | 0 | 0 | 1 | 0 | | 930 |
| I_0 | 0 | 1 | 0 | 0 | | 940 |
| I_3 | 1 | 0 | 0 | 0 | | 950 |
| | O_3 | O_2 | O_1 | O_0 | | |

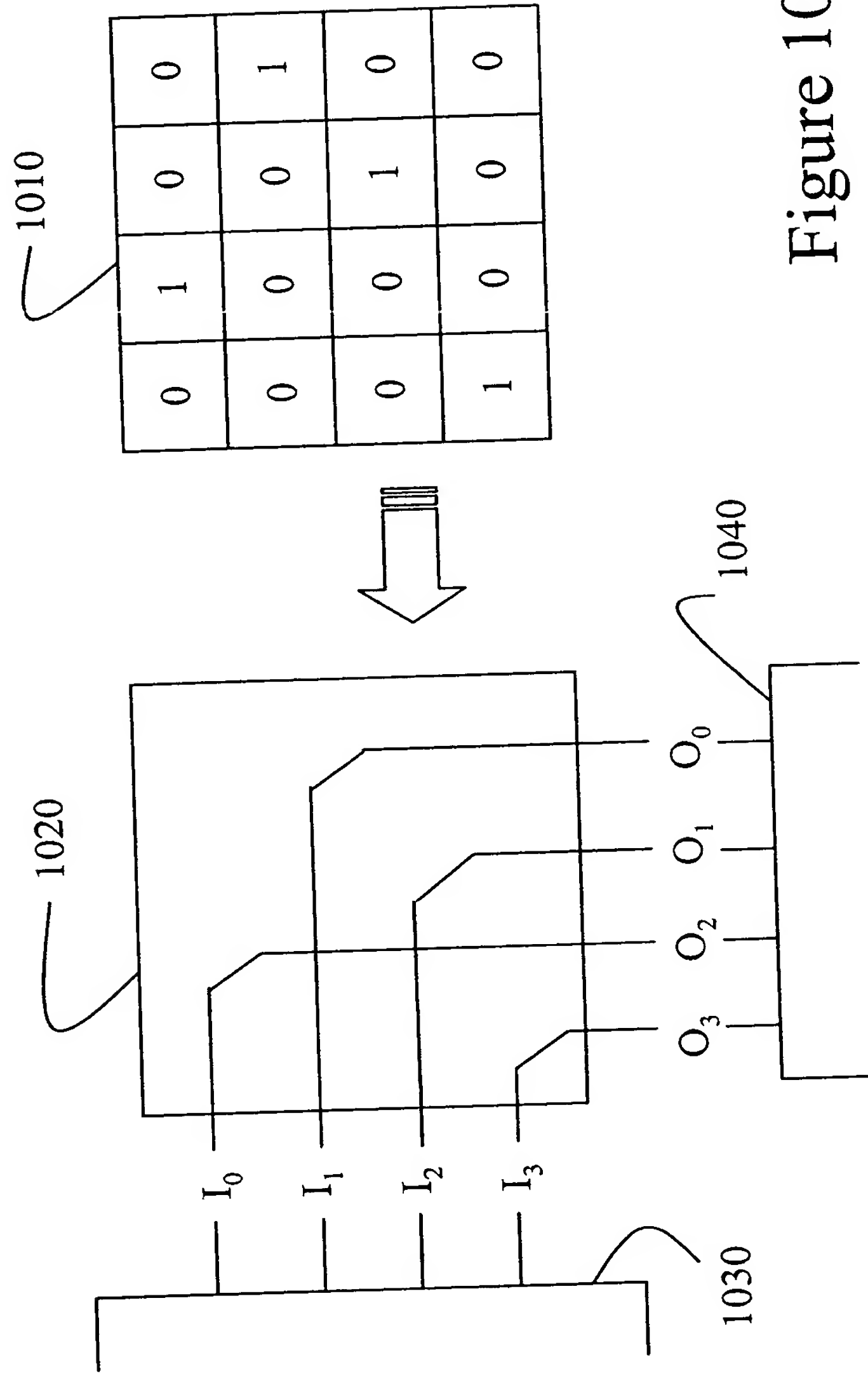


Figure 10

Figure 11

Figure 11 is a diagram showing a 6x8 grid of binary data. The grid is divided into four groups of two columns each, labeled 1140, 1130, 1130, and 1130 from left to right. A dashed box labeled 1110 encloses the first four columns. A thick horizontal line labeled 1120 runs across the middle of the grid, separating the first two rows from the last two rows.

| | | | | | | | |
|-----|---|---|---|---|---|---|---|
| | | | | | | | |
| ... | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| ... | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| ... | 0 | 0 | 0 | 0 | 1 | 0 | 0 |
| ... | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| | | | | | | | |

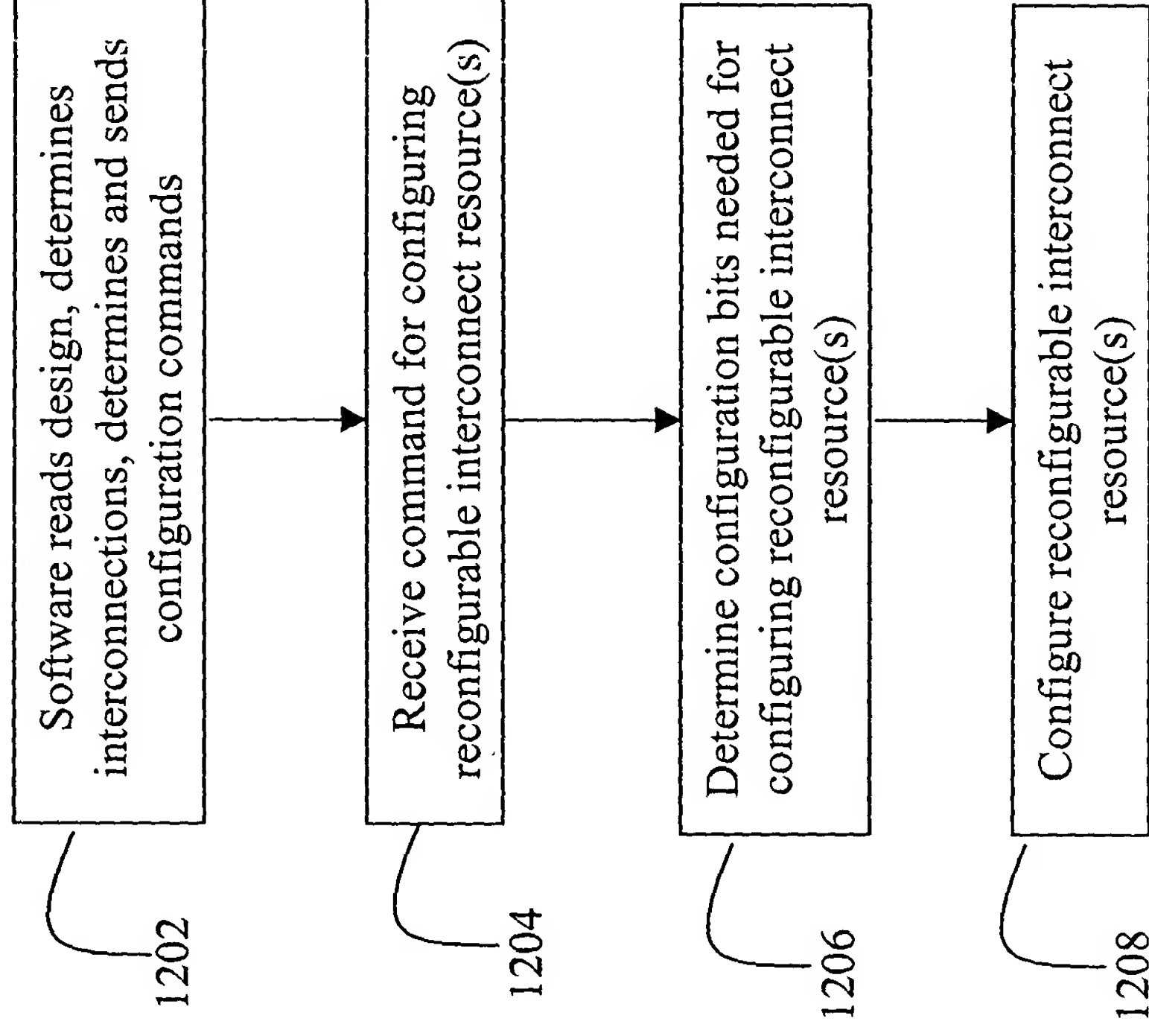


Figure 12

